

## TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

<b>Title of Invention</b>	In-Line D.C. Testing of Multiple Memory Modules in a Panel Before Panel Separation
Application Number :	
Date :	
First Named Applicant:	Ramon S. Co
Confirmation Number:	
Attorney Docket Number:	KT-10A

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Documents being submitted:	Files
us-fee-sheet	KT10aex-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-request	KT10aex-usrequ.xml us-request.dtd us-request.xsl
application-body	KT10ae-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblk.dtd fgKT10e01.TIF fgKT10e02.TIF fgKT10e03.TIF fgKT10e04.TIF fgKT10e05.TIF fgKT10e06.TIF fgKT10e07.TIF fgKT10e08.TIF OathKT10.tif
us-declaration	
<b>Comments</b>	